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State Machine Modeling of TPED and TPPU

Mr. Ron Funk (DRDC) Mr. Rick Sorensen (Vitech Corp.) NDIA 8th Sys Engr Conference, San Diego 27 October 2005

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Outline of Presentation



- Operational Activity Modeling
 - Task, Process, Exploit, Disseminate (TPED)
 - Task, Post, Process, Use (TPPU)
- State Machine Concepts
- Description of TPPU State Machine
- Modeling Process
- Examples
- Q&A













How TPED/TPPU Deal With Situation Awareness (SA)



- <u>TPED</u>
 - Centralized control of all activity
 - Only describes assigned jobs
 - Worker utilization maximized when jobs are 100% of time
 - No way for operators to do SA except as a separate job
- <u>TPPU</u>
 - Decentralized and uncoordinated independent activities
 - Becomes self synchronizing when queue checked frequently
 - Cycle based on doing jobs and maintaining SA
 - Jobs are composed of TPP, but not Use
 - Use is residual time when no jobs (i.e. 100% TPP)
 - SA automatically distributed everywhere as Use
 - Effectiveness is maximized when job utilization < 100%











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TPPU Business Rules Being Modeled



- Concurrent Independent Cycles are modeled:
 - Cycles automatically repeated
 - Activity logic is easily amended
 - Model is scalable
- <u>Improved realism of business rules</u> by explicitly modeling:
 - Regular checking of queue for highest priority job
 - Pre-empted jobs returned to queue to complete later
 - Credited for work already completed
 - Job status tracked continuously once it enters queue
 - Checks remaining time needed to complete job
 - Jobs abandoned once expected completion time exceeds ٠ deadline



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DODAF Operational Activities (OV-5) Modeling Efforts



- Summer 2003
 - Conceptual TPED from first principles
 - Elegant model but too conceptual to actually work
- Spring 2004
 - Data Fusion Using TPED
 - Integrated behavior of basic TPED components
 - Data stovepipes shown but no description of fusion
- Summer 2004
 - Integrated ISR Architecture Examples
 - Modeled sequence of C2 changes for UAV flight
 - TPPU used to describe OPCEN activities
- Spring 2005 to Present
 - Describe TPPU as State Machine
 - Articulate TPPU with robust business rules
 - Model can also handle TPED









CORE Model of UAV C2 Changes: Summer 2004





Initial CORE Model of TPPU Job Activity



Example of Top-Level Model





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Modeling the Processes Using CORE



- CORE used to capture behavior models
 - Document known processes
 - Capture timelines, data flow, business rules
 - Expose unknowns for further exploration
- COREsim used to execute models
 - Validate our understanding
 - Exercise to-be processes



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Process to Efficiently Build Behavior & SM Models



- 1. Threads : Work process of each job by an operator
 - Articulates specific job activities for each operator
- 2. Integrated : Common themes between jobs & operators
 - Calculates minimum resource demand
- 3. Allocated : Differentiation of skills
 - Added cost of specialization
 - Determine any offsets when generic work is done during idle time
- 4. State Machine : Schedule and track concurrent activity
 - Impact of Business Rules (i.e. Job priorities & time remaining)
 - <u>Caution</u>: Logic is not as visible to users as usual behavior models







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State Machine Characteristics



- State Machine has the following key characteristics:
 - <u>Initial state</u> or record of something stored someplace
 - Set of possible <u>input</u> events
 - Set of <u>new states</u> that may result from the input
 - Set of possible <u>actions or output</u> events that result
 - It is composed of the following elements:
 - Description of the initial state;
 - Set of states;
 - Set of input events;
 - Set of output events;
 - Function that maps states and input to output;
 - State transition function.



CORE Model of State Machine: Overall Structure





- Excel spreadsheets are used to store model data for:
 - Schedule of Events (i.e. time and location of next work item)
 - Initial Status of Threads (i.e. result of occurred before)
 - Thread Attributes (i.e. duration of work)

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Event states and schedule updated using COREscript





How Specialization Can Affect Personnel Requirements



| Functionality | Original Personnel Demand (No Transfer) | Specialists Do Generic Work 50% of Idle Time | Personnel Required (if 50% transferred) | Specialists Do Generic Work 100% of Idle Time | Personnel Required (if 100% transferred) | | |
|----------------------|--|---|--|--|---|--|--|
| Generic | 3.2 | -0.7 | 2.5 | -1.4 | 1.8 | | |
| Specialist Type 1 | 0.2 | +0.4 | 0.6 | +0.8 | 1.0 | | |
| Specialist Type 2 | 1.4 | +0.3 | 1.7 | +0.6 | 2.0 | | |
| Bodies Needed | 7 | | 6 | | 5 | | |

NOTE: Have to round up to account for fractional headcounts. 3.2 becomes 4, 0.2 becomes 1, 1.4 becomes 2, and total equals 7!



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Other Aspects to Explore



- Our target environment has clear notion of interruptible processes. Need to explore:
 - Impacts of interrupts on timelines
 - Business rules for interrupt handling
- Our target environment exhibits queuing of products. Need to explore:
 - Queuing logic











Interrupt Model v2 - Adding Impacts of Queuing













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Task Queue Processing - Execution









Process to Efficiently Build Behavior & SM Models



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State Machine Form



- Orthogonal to FFBD* forms
 - <u>FFBDs</u> are single-threaded, single-instance temporal domains
 - <u>State machine</u> is multi-threaded, multi-instance
- Essentially an 'engine' for processing scenarios
 - Task flow, resource, and queue logic in model
 - Tasks, resources, timelines instantiated at runtime
- Uses pre- and post- processing (Excel)

*FFBD: Functional Flow Block Diagram





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Execution Results – Example

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| 4 | 0:00:02 | 1 | Assess Input | 0:1 | 0:43 | 0:00:01 | nil | nil | nil | nil | r il | nil | 9 | * | | |
| 5 | 0:00:03 | 1 | Assess Input | 0:1 | 0:43 | 0:00:01 | nil | nil | nil | nil | r il | nil | 8 | | | |
| 6 | 0:00:04 | 1 | Assess Input | 0:1 | 0:43 | 0:00:01 | nil | nil | nil | nil | r il | nil | 7 | | | |
| 7 | 0:00:05 | 1 | Assess Input | 0:1 | 0:43 | 0:00:01 | nil | nil | nil | nil | r il | nil | 6 | | | |
| 8 | 0:00:06 | 1 | Assess Input | 0:1 | 0:43 | 0:00:01 | nil | nil | nil | nil | ril | nil | - 6 | | | |
| 9 | 0:00:07 | 1 | Assess Input | 0:1 | 0:43 | 0:00:01 | nil | nil | nil | nil | ril | nil | 4 | | | |
| 10 | 0:00:08 | 1 | Assess Input | 0:1 | 0:43 | 0:00:01 | nil | nil | nil | nil | ril | nil | 3 | | | |
| 11 | 0:00:09 | 1 | Assess Input | 0:1 | 0:43 | 0:00:01 | nil | nil | nil | nil | r il | nil | 2 | | | |
| 12 | 0:00:10 | 1 | Assess Input | 0: | 0:43 | 0:00:01 | nii 0.00.44 | nii | nii | nii | n II | nii | 1 | | | |
| 13 | 0:00:11 | 1 | Make Local Copy | 0: | 0:43 | 0:00:01 | 0:00:11 | nii 0.00.10 | nii | nii | | nii | 1 | | | |
| 14 | 0:00:12 | 1 | Assess Local Copy | 0.1 | 0:43 | 0:00:01 | 0:00:11 | 0:00:12 | nii | nii | | nii | 14 | | | |
| 16 | 0:00:13 | 1 | Assess Local Copy | 0.0 | 0.43 | 0:00:01 | 0.00.11 | 0.00.12 | nil | nil | 1 | nil | 13 | | | |
| 17 | 0:00:15 | 1 | Assess Local Conv | 0.0 | 0:43 | 0:00:01 | 0:00:11 | 0:00:12 | nil | nil | i i | nil | 12 | | | |
| 18 | 0:00:16 | 1 | Assess Local Copy | 0:1 | 0:43 | 0:00:01 | 0:00:11 | 0:00:12 | nil | nil | 1 | nil | 11 | | | 1 |
| 19 | 0:00:17 | 1 | Assess Local Copy | 0:1 | 0:43 | 0:00:01 | 0:00:11 | 0:00:12 | nil | nil | ril | nil | 10 | | | 1 |
| 20 | 0:00:18 | 1 | Assess Local Copy | 0:1 | 0:43 | 0:00:01 | 0:00:11 | 0:00:12 | nil | nil | ril | nil | 9 | | | |
| 21 | 0:00:19 | 1 | Assess Local Copy | 0:1 | 0:43 | 0:00:01 | 0:00:11 | 0:00:12 | nil | nil | r il | nil | 8 | | | |
| 22 | 0:00:20 | 1 | Assess Local Copy | 0:1 | 0:43 | 0:00:01 | 0:00:11 | 0:00:12 | nil | nil | r il | nil | 7 | | | |
| 23 | 0:00:21 | 1 | Assess Local Copy | 0:0 | 0:43 | 0:00:01 | 0:00:11 | 0:00:12 | nil | nil | r il | nil | 6 | | | |
| 24 | 0:00:22 | 1 | Assess Local Copy | 0:1 | 0:43 | 0:00:01 | 0:00:11 | 0:00:12 | nil | nil | r il | nil | 5 | | | |
| 25 | 0:00:23 | 1 | Assess Local Copy | 0:1 | 0:43 | 0:00:01 | 0:00:11 | 0:00:12 | nil | nil | r il | nil | 4 | | | |
| 26 | 0:00:24 | 1 | Assess Local Copy | 0:1 | 0:43 | 0:00:01 | 0:00:11 | 0:00:12 | nil | nil | 1 | nil | 3 | | | |
| 27 | 0:00:25 | 1 | Assess Local Copy | 0:1 | 0:43 | 0:00:01 | 0:00:11 | 0:00:12 | nil | nil | | nil | 2 | | | |
| 28 | 0:00:26 | 1 | Assess Local Copy | 0:1 | 0:43 | 0:00:01 | 0:00:11 | 0:00:12 | nil | nil | | nil | 1 | | | |
| 29 | 0:00:27 | 1 | Assessment Text | 0:1 | 0:43 | 0:00:01 | 0:00:11 | 0:00:12 | 0:00:27 | nil | | nil | 10 | | | |
| 30 | 0:00:28 | 1 | Assessment Text | 0: | 0:43 | 0:00:01 | 0:00:11 | 0:00:12 | 0:00:27 | nii | 7 | nii | 9 | | | |
| 31 | 0:00:29 | 1 | Assessment lext | 00 | u:45 | 0:00:01 | 0:00:11 | 0:00:12 | 0:00:27 | nii | /11 | nii | 8 | | | 1 |



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Recap



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- State Machine Concepts
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- Way Ahead





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Execution Scenario 1

Normal Completion



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Scenario 1-3











Scenario 1-6













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Execution Scenario 2

Job Abandoned



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Scenario 2-3







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Scenario 2-6











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Scenario 2-8











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